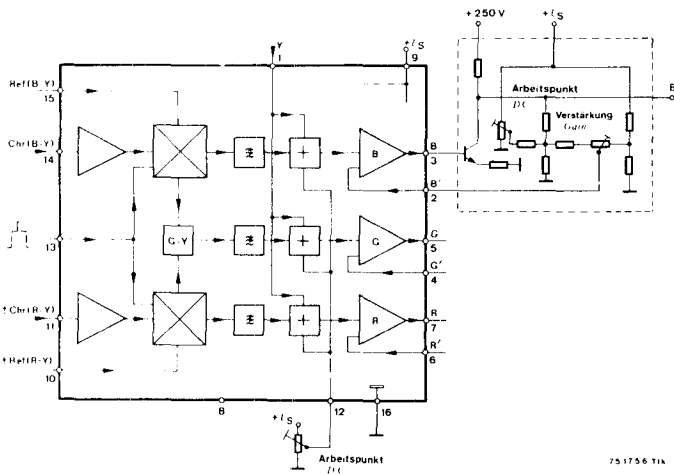


Integrated circuit for RF application

TDA 2160

Synchronous demodulator and RGB matrix for colour TV receivers

Supply voltage	U_S	< 13,2	V
Power dissipation	P_{tot}	0,8	W
Luminance input resistance	Pin 1	R_i	50 k Ω
Blanking input pulse	Pin 13	I_i	> 10 μ A
Amplification ratio		$\frac{A(B-Y)}{A(R-Y)}$	1,78
Gain of luminance channels including video power stages		A_U	100



Features:

- High stability of the DC output voltage ensured by applying heavy feedback from the output stages
- Large bandwidth
- Low subcarrier leakage ensured by means of integrated active filters
- Large range of the output black level adjustments
- Large dynamic swing of the output signals
- Good electrical stability of the RGB amplifiers ensured by internal frequency compensations

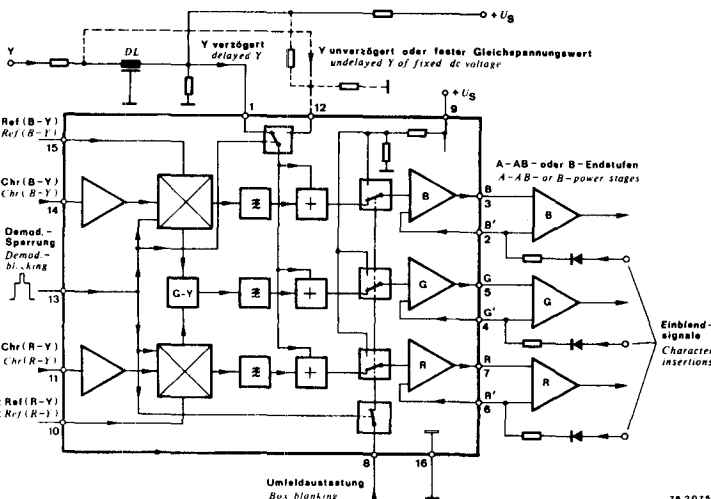
Case:

20 A 16 DIN 41866
JEDEC MO 001 AC
DIP 16-lead
Dimensions see page 60
Number 7

TDA 2161

Synchronous and RGB matrix for colour TV receivers

Supply voltage	U_S	12	V
Power dissipation	P_{tot}	0,8	W
Luminance input resistance	Pin 1	R_i	50 k Ω
Blanking input pulse	Pin 13	I_i	> 10 μ A
Gain ratio		$\frac{A(B-Y)}{A(R-Y)}$	1,78
Gain of luminance channels including video power stages		A_U	100
Field black out pulse	Pin 8	U_i	2.8 ... 5 V



Features:

- High stability of the DC output voltage ensured by applying heavy feedback from the output stages
- Large bandwidth
- Good electrical stability of the RGB amplifiers ensured by internal frequency compensations
- Field black out for signal fade-in

Case:

20 A 16 DIN 41 866
JEDEC MO 001 AC
DIP 16-lead
Dimensions see page 60
Number 7