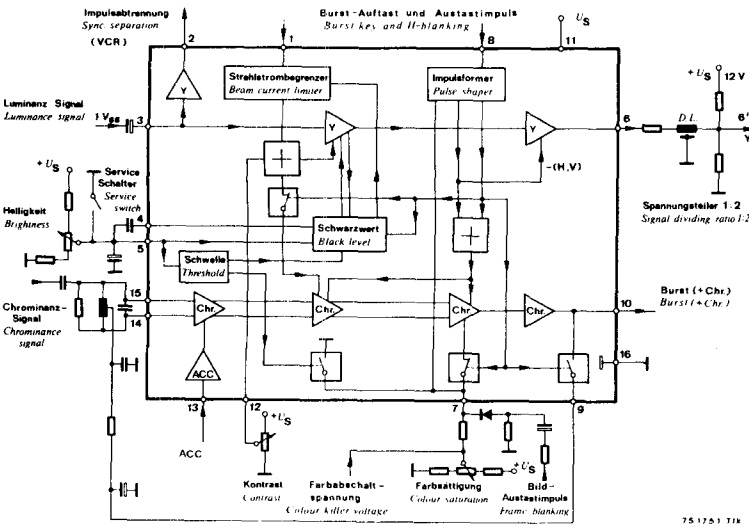


Integrated circuits for RF applications

TDA 2150

Luminance and chrominance amplifier for colour TV receivers

Supply voltage	U_S	12	V
Power dissipation	P_{tot}	0,8	W
Luminance input signal 100% white beam	Pin 3 U_i	1	V _{ss}
Luminance output signal $U_{12} = 12\text{ V}$	Pin 6 U_q	2,0 ... 2,4	V _{ss}
Chrominance input signal	Pin 14-15 U_i	< 80	mV _{ss}
Chrominance output signal $k \geq 5\%$	Pin 10 U_q	3	V _{ss}



75.17.51 T1K

Features:

- Very low spread of the D.C. controls for brightness, contrast and colour saturation in order to avoid pre-settings completely
- Tracked D.C. contrast control in chrominance and luminance channels
- Independent video signal output for the sync. separator especially at VCR playback operation
- Generation of a luminance service signal for adjusting the CRT cathode bias

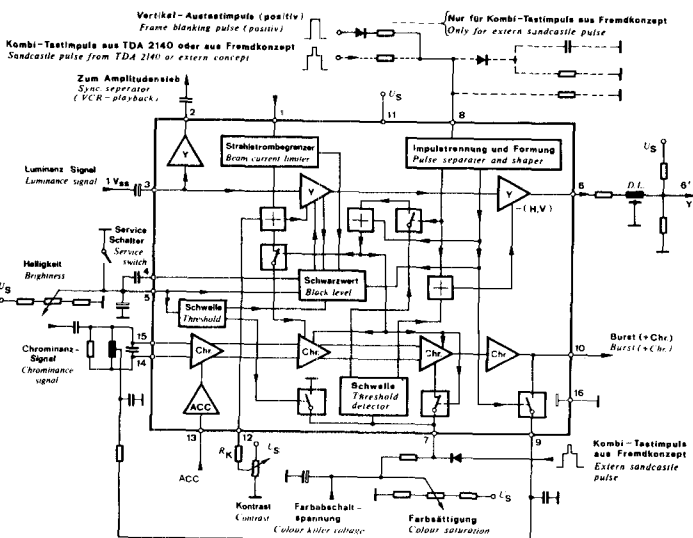
Case:

20 A 16 DIN 41866
JEDEC MO 001 AC
DIP 16-lead
Dimensions see page 60
Number 7

TDA 2151

Luminance and chrominance amplifier for colour TV receivers

Supply voltage	U_S	12	V
Power dissipation	P_{tot}	0,8	W
Luminance input signal 100% white beam	Pin 3 U_i	1	V _{ss}
Luminance output signal $U_{12} = 12\text{ V}$	Pin 6 U_q	2,0 ... 2,4	V _{ss}
Chrominance input signal	Pin 14-15 U_i	< 80	mV _{ss}
Chrominance output signal $k \geq 5\%$	Pin 10 U_q	3	V _{ss}
Contrast reduction at beam current limiter action		25 ... 80	%
Programming resistor	Pin 12 R_k	0 ... 7,5	k Ω



78.2078

Features:

- Very low spread of the DC controls of brightness, contrast and colour saturation in order to avoid pre-settings completely
- Tracked DC, contrast control in chrominance and luminance channels
- Generation of a luminance service signal for adjusting the CRT cathode bias
- Compatible with every available concept in the market for combined test pulse generation

Case:

20 A 16 DIN 41 866
JEDEC MO 001 AC
DIP 16-lead
Dimensions see page 60
Number 7